

EDITORIAL CONTACT:

David Grabove
(719) 278-3540 Ext 10
david.grabove@futureplus.com

RELEASE DATE:

January 29, 2007

New Tool Delivers Low-cost Jitter Analysis

**Performs physical-layer tests while the target Serial RapidIO®
or PCI Express® system is processing real bus traffic at up to 3.125 Gb/s.**

FuturePlus Systems Corp. today introduced a low-cost jitter injection and analysis tool that performs physical layer jitter evaluation in conjunction with protocol analysis. The FS5000 Jitterlyzer™ can perform physical-layer tests while the target Serial RapidIO® (sRIO) or PCI Express® (PCIe®) system is processing real bus traffic at up to 3.125 Gb/s. The new tool allows quick, cost effective correlation between protocol failures and physical-layer signal integrity.

The FS5000 generates and characterizes jitter in an inexpensive way that makes advanced jitter analysis available to all engineers. It will be used by engineers involved with debug and validation, system integrity verification, physical layer analysis, and component design, who would otherwise need a multi-Gigahertz oscilloscope or a bit error ratio tester (BERT) to take physical-layer measurements. Such test instruments typically cost several times the price of the FS5000.

Software controls the FS5000 over a USB connection

The FS5000 includes Tool Manager software that runs on a PC. It connects to the FS5000 over a USB connection. The software's graphic user interface (GUI) permits users to set up parameters for the injected jitter, and to view bathtub curves and eye diagrams on the PC display.

Versatile Choice of Connection Methods

Interposer probes and connectorless probe adapters offer flexibility of connection methods.

-- more --

For PCIe applications, four interposers permit X4 or X1 capture of motherboard-transmit-to-adapter-card receive, or adapter-card-transmit-to-motherboard-receive. (Interposer probes are designed for insertion into a PCIe slot, while also accepting an active adapter card in the probe's extender slot.)

For sRIO applications, (as well as embedded PCIe applications that do not have slot connectors), users may design a connectorless footprint onto the target system, for use with a high-density, compression probe.

Key Features

- PC-based Tool Manager software controls the FS5000 over a USB connection.
- Gives insight into a live system using real traffic at up to 3.125 Gb/s.
- Interposer probes allow jitter measurement from the motherboard to the adapter card or vice versa, while also simultaneously making protocol measurements of the same traffic on a logic analyzer
- High density cable connection from the interposer to SMA connectors on the tool.
- Easier to use, faster, no complex system to set up.

Price and Delivery

Model	Description	Price
FS1050	PCIe X4 Motherboard Tx to Adapter Card Rcv Interposer	\$3,000
FS1051	PCIe X4 Adapter Card Tx to Motherboard Rcv Interposer	\$3,000
FS1052	PCIe X1 Motherboard Tx to Adapter Card Rcv Interposer	\$3,000
FS1053	PCIe X1 Adapter Card Tx to Motherboard Rcv Interposer	\$3,000
FS1054	SMA to Samtec Cable	\$1,500
FS5000	<i>Jitterlyzer™</i> Jitter Injection and Analysis Tool	\$35,000

Jitterlyzer is a trademark of FuturePlus Systems Corp.

About FuturePlus Systems

FuturePlus Systems Corporation is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is an Agilent Technologies channel partner. FuturePlus Systems products are also supported by authorized resellers in Canada, China, Taiwan, Japan, India, Italy, United Kingdom and most European countries. More information about FuturePlus Systems Corporation may be found on the internet at www.futureplus.com.

#

NOTE TO THE EDITOR: Sales information may be obtained at the address or phone number listed below. If you choose to review this item, fastest response to your readers' inquiries will be assured by mailing them to the following address:

FuturePlus Systems Corporation
6455 N. Union Blvd. Suite 202
Colorado Springs, CO 80918-5844
TEL: (719) 278-3540
Email via link at www.futureplus.com