



Intelop announces industry's first customizable TCP-Offload Engine at 2-Gpbs with integrated Ethernet MAC Silicon IP for SOC, ASIC and FPGA customers in Network equipment, Network Security, Telecom, SAN and NAS markets

Customers will be able to customize this TOE engine by adding differentiable features and integrate this synthesizable core in the design of SOCs/ASICs/FPGAs to develop enhanced differentiable products.

Worlds first integrated TOE+2G bit Ethernet MAC silicon IP with security features provides enhanced functionality in Layer-2-7 Switches/Routers, IPS/IDS appliances and Network Security appliances. Advanced architecture with built in scalability allows customers to target this to many silicon libraries ranging from 0.18 um to 0.090 nm ASIC, SOC or FPGA without compromising performance or functionality.

Santa Clara, California – August 28, 2006

Intelop Corporation, a leading IP developer, customization & engineering services provider, today announced development of the worlds first TCP offload engine/TCP accelerator, with integrated Ethernet MAC running at 2 Gbps sustained rates. It can be scaled down to 100 Mbps with fewer features also.

"Because of its advanced scalable architecture, it can be customized to implement differentiated features and performance requirements to meet customer's specifications e.g. choice of PHY interface - XGMII or Serial, scalable packet FIFO size, scalable size of Session Management table, Session Parameters, Flags/protocol processing, scalable size of direct store Packet memories, integrated DDR/SSRAM controllers and many other features," said Kelly Masood, President of Intelop.

The IP core can be used in the appliances ranging from high-end Network Intrusion Detection/Prevention, Network Security, Content Protection/Security, Network Switches appliances and Routers to low-cost Layer-2/3 switching/routing equipment.

"We utilized our expertise in designing highly successful and advanced technology Multi-Giga bit Enterprise-class IDS/IPS, Network Security appliances employing SOCs also designed by intelop in defining the architecture of this TOE engine," said Kelly Masood. This and other IP components are central to many new SOCs/ASICs/FPGAs targeting Networking, Network Security and Storage markets. Intelop also integrated many of these IPs with other standard blocks in SOCs and developed necessary software as total turn key solutions.

"We are excited about this new crown jewel and the ability to develop value-added networking silicon and total solutions for our customers." said Kevin Moore of Intelop.

Intelop Corporation is a custom IP developer, SoC/ASIC integrator and engineering services provider for Networking, Network Security, storage and embedded systems. The company's silicon-proven semiconductor IP with comprehensive hardware and software experience has helped build several complete solutions.

Visit: <http://www.intelop.com/>

Intelop Corporation

Ph: 408-496-0333, Fax: 408-496-0444

info@intelop.com